

UNISONIC TECHNOLOGIES CO., LTD

# UMY1N

Preliminary

**DUAL TRANSISTOR** 

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## DESCRIPTION

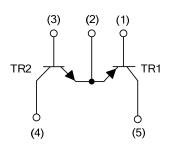
The UTC **UMY1N** is a dual transistor, including a NPN and a PNP which have common emitters. it uses UTC's advanced technology to provide customers with high DC current gain, etc.

## FEATURES

\* PNP and NPN transistors have common emitters

\* High DC current gain

## EQUIVALENT CIRCUIT

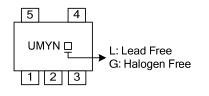


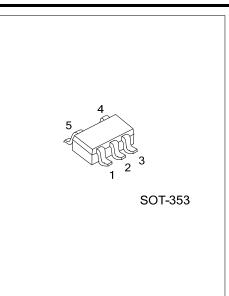
### ORDERING INFORMATION

Order	Order Number		Pin Assignment					Dealise	
Lead Free	Halogen Free	Package	1	2	3	4	5	Packing	
UMY1NL-AL5-R	UMY1NG-AL5-R	SOT-353	B1	E1, E2	B2	C2	C1	Tape Reel	

UMY1NL- <u>AL5</u> -R T	(1)Packing Type	(1) R: Tape Reel	
	(2)Package Type	(2) AL5: SOT-353	
	(3)Lead Free	(3) L: Lead Free, G: Halogen Free	

#### MARKING INFORMATION





#### ■ ABSOLUTE MAXIMUM RATINGS (T<sub>A</sub>=25°C, unless otherwise specified)

DADAMETED	0)(MDO)	RAT			
PARAMETER	SYMBOL -	TR1	TR2	UNIT	
Collector-Base Voltage	V <sub>CBO</sub>	-60	60	V	
Collector-Emitter Voltage	V <sub>CEO</sub>	-50	50	V	
Emitter-Base Voltage	V <sub>EBO</sub>	-6	7	V	
Continuous Collector Current	lc	-150	150	mA	
Collector Power Dissipation	Pc	150		mW	
Junction Temperature	TJ	150		°C	
Storage Temperature	T <sub>STG</sub>	-55~+150		°C	

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

#### ■ **ELECTRICAL CHARACTERISTICS** (T<sub>A</sub>=25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT	
TR1							
Collector-Base Breakdown Voltage	BV <sub>CBO</sub>	I <sub>C</sub> =-50μΑ, I <sub>E</sub> =0	-60			V	
Collector-Emitter Breakdown Voltage	BV <sub>CEO</sub>	I <sub>C</sub> =-1mA, I <sub>B</sub> =0	-50			V	
Emitter-Base Breakdown Voltage	BV <sub>EBO</sub>	I <sub>E</sub> =-50μΑ, I <sub>C</sub> =0	-6			V	
Collector Cut-Off Current	I <sub>CBO</sub>	V <sub>CB</sub> =-60V, I <sub>E</sub> =0			-0.1	μA	
Emitter Cut-Off Current	I <sub>EBO</sub>	V <sub>EB</sub> =-6V, I <sub>C</sub> =0			-0.1	μA	
Collector-Emitter Saturation Voltage	V <sub>CE(SAT)</sub>	I <sub>C</sub> =-50mA, I <sub>B</sub> =-5mA			-0.5	V	
DC Current Gain	h <sub>FE</sub>	V <sub>CE</sub> =-6V, I <sub>C</sub> =-1mA	120		560		
Transition Frequency	f⊤	V <sub>CE</sub> =-12V, I <sub>E</sub> =-2mA, f=100MHz		140		MHz	
Output Capacitance	C <sub>ob</sub>	V <sub>CB</sub> =-12V, I <sub>E</sub> =0, f=1MHz			5	рF	
TR2							
Collector-Base Breakdown Voltage	<b>BV</b> <sub>CBO</sub>	I <sub>C</sub> =50μA, I <sub>E</sub> =0	60			V	
Collector-Emitter Breakdown Voltage	BV <sub>CEO</sub>	$I_{C}=1mA$ , $I_{B}=0$	50			V	
Emitter-Base Breakdown Voltage	$BV_{EBO}$	I <sub>E</sub> =50μA, I <sub>C</sub> =0	7			V	
Collector Cut-Off Current	I <sub>CBO</sub>	$V_{CB}$ =60V, I <sub>E</sub> =0			0.1	μA	
Emitter Cut-Off Current	I <sub>EBO</sub>	$V_{EB}=7V, I_{C}=0$			0.1	μA	
Collector-Emitter Saturation Voltage	V <sub>CE(SAT)</sub>	I <sub>C</sub> =50mA, I <sub>B</sub> =5mA			0.4	V	
DC Current Transfer Ratio h <sub>FE</sub>		V <sub>CE</sub> =6V, I <sub>C</sub> =1mA	120		560		
Transition Frequency f <sub>T</sub>		V <sub>CE</sub> =12V, I <sub>E</sub> =2mA, f=100MHz		180		MHz	
Output Capacitance	Cob	V <sub>CB</sub> =12V, I <sub>E</sub> =0, f=1MHz			3.5	pF	



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